



## DAVID DSP processor board

### Technical Characteristics

Type	#	Description
digital I/O	30	FPGA Typical program : <ul style="list-style-type: none"> <li>• 3 phase PWM modulator</li> <li>• Interface for an incremental encoder with 2 signals in quadrature</li> </ul>
	8	Simple I/O
Analog IN	14	12 bits 600ns conversion time 0-4.096V passive shaping circuit for other ranges (-10 .... 10V )
Analog OUT	4	10 bits
Serial	1	RS232
JTAG	1	DSP Debugging connector
DSP	32 bits 32 MHz 48 bits 4 Mbits 100 MFLOPS	floating point processor instruction execution rate. Extension to 40MHz Instructions word-length RAM
LINK	1	Processor link port ( see users manual of ADSP 210xx ). Connection of two processors for data exchange
Temperature	0-55°	Commercial range. Special execution for industrial range possible
Dimensions	235x160x25	mm
Weight	235g	
Software language	ANSI C	See ANALOG DEVICES documentation

All characteristics are subject to change without notice